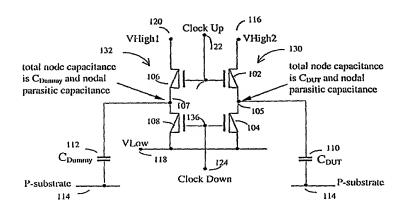
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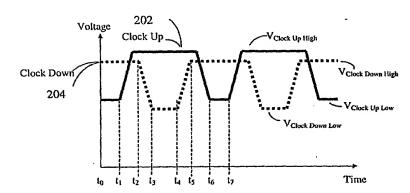
JCKED BASED METHOD AND DEVICES FOR MEA. JRING VOLTAGE-VARIABLE CAPACITANCES AND OTHER ON-CHIP PARAMETERS Inventor(s): David Michael Rogers, et al., Attorney Docket No.: 9076/70

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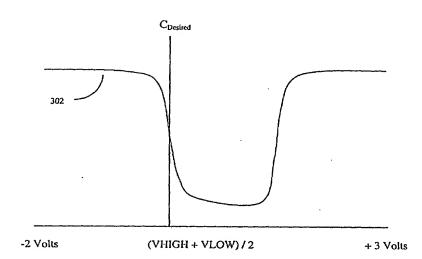


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FIG.3

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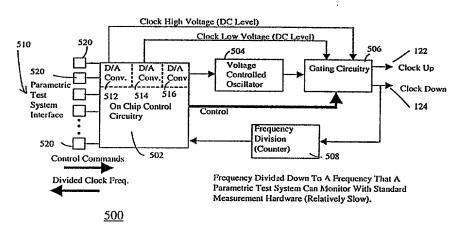
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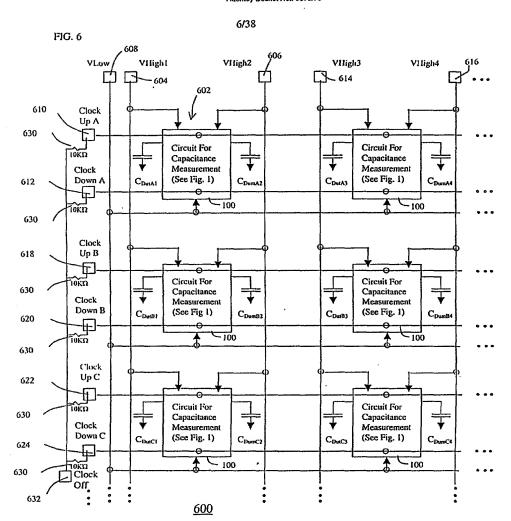
FIG. 5

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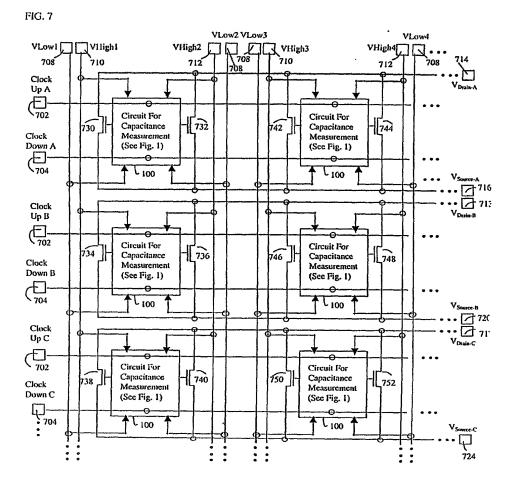
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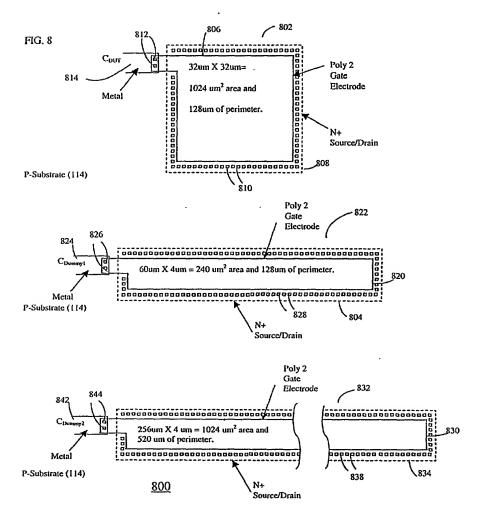
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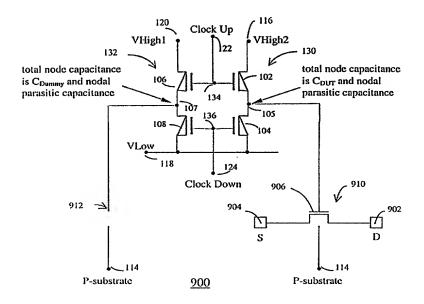
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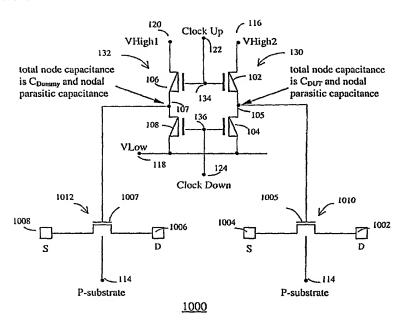


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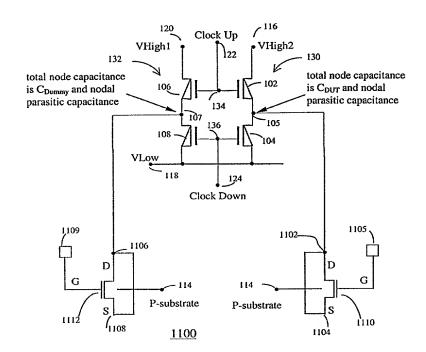
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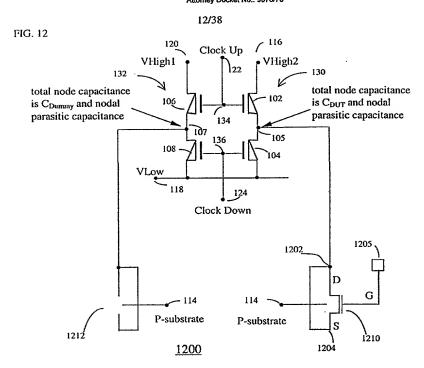


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FIG. 11

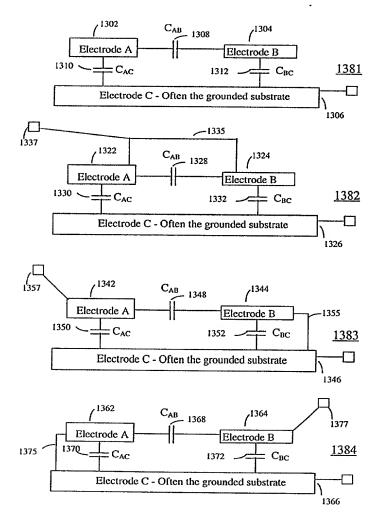


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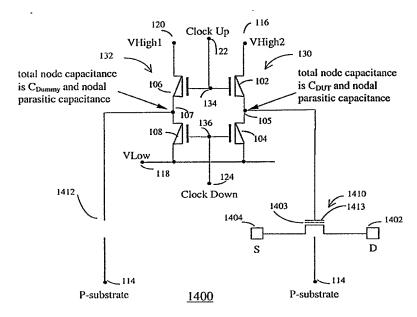
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FIG. 13



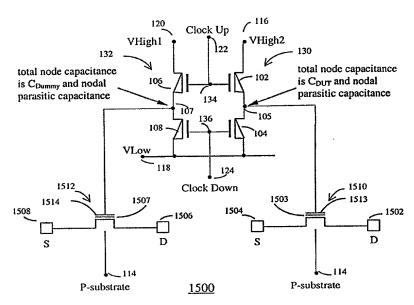
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FIG. 14



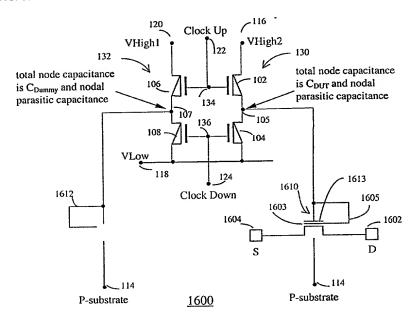
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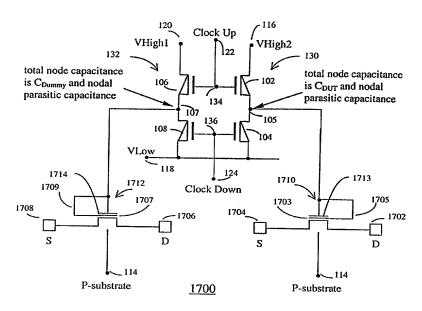
Patent Application for. CLUCKED BASED METHOD AND DEVICES FOR MEAS... ING VOLTAGE-VARIABLE CAPACITANCES AND OTHER ON-CHIP PARAMETERS Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

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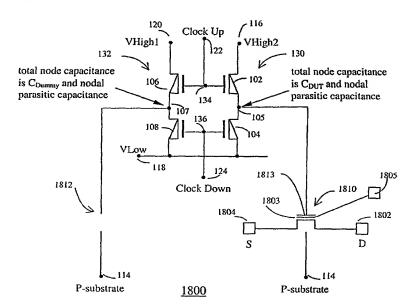
Patent Application for: CLOCKED BASED METHOD AND DEVICES FOR MEASURING VOLTAGE-VARIABLE CAPACITANCES AND OTHER ON-CHIP PARAMETERS Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

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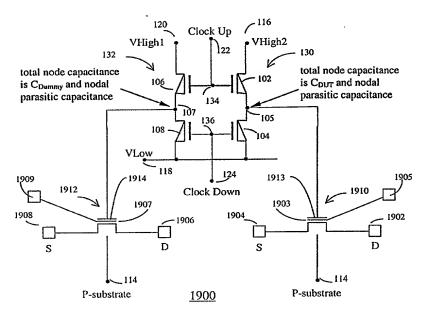
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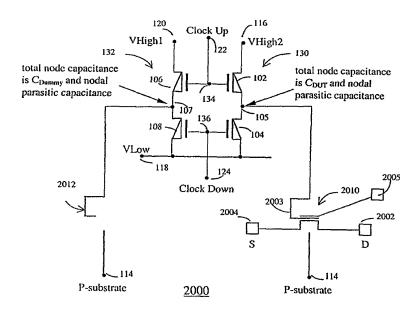
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Attorney Docket No.: 9076/70

FIG. 19



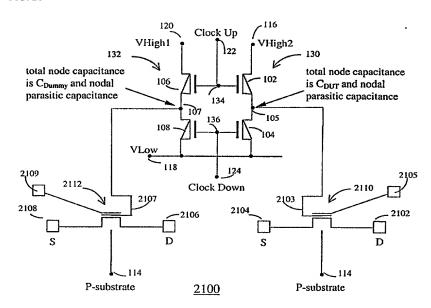
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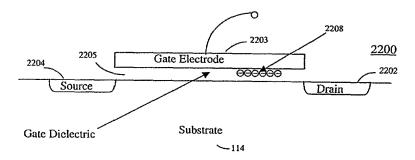
Patent Application for: CLOCKED BASED METHOD AND DEVICES FOR MEASURING VOLTAGE-VARIABLE CAPACITANCES AND OTHER ON-CHIP PARAMETERS Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

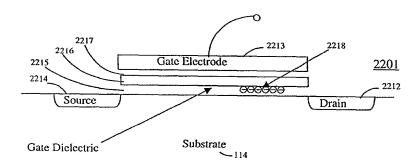
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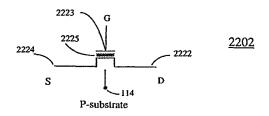


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FIG. 22



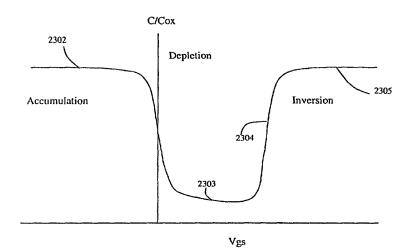




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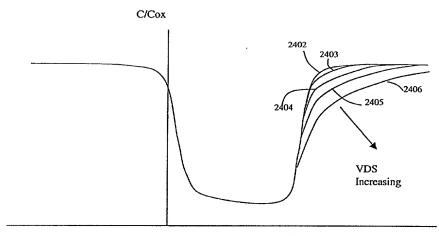
FIG. 23:



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FIG. 24



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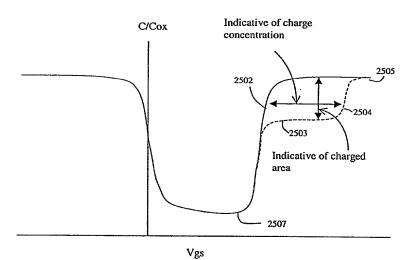
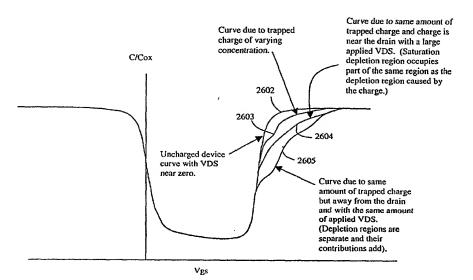


FIG. 26

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FIG.27

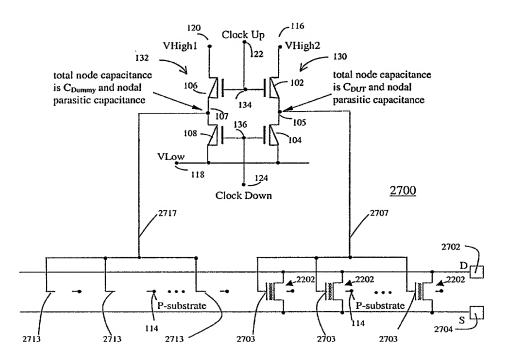
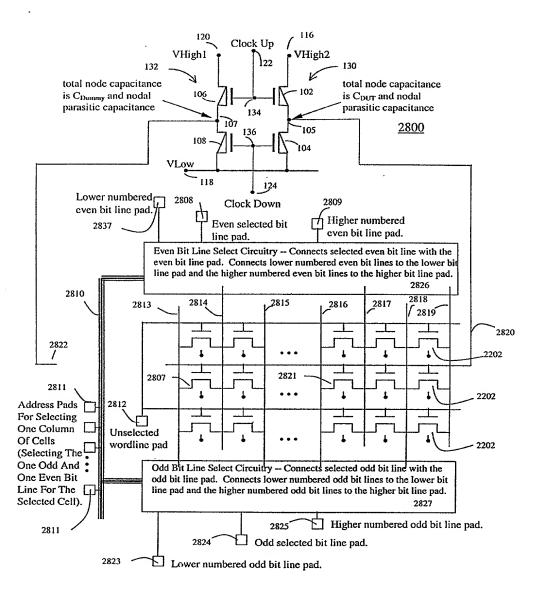


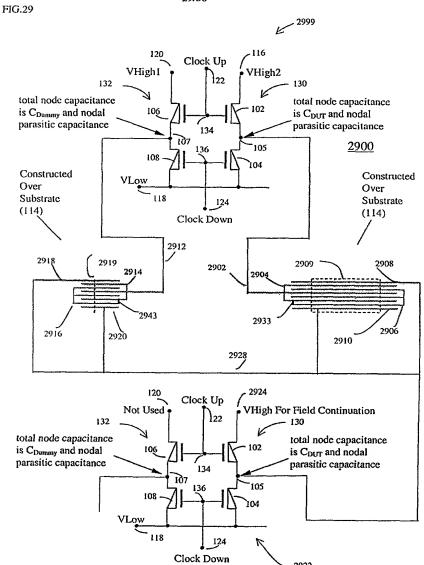
FIG.28



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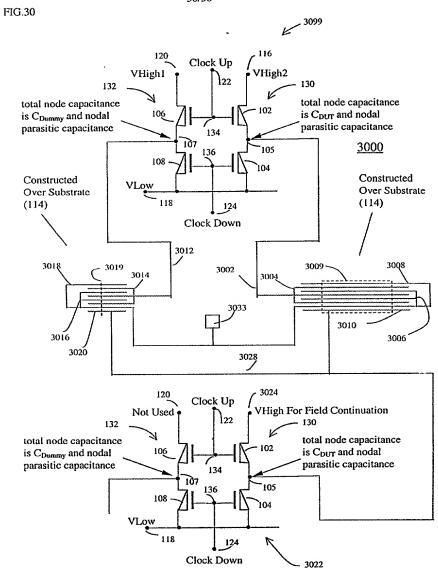
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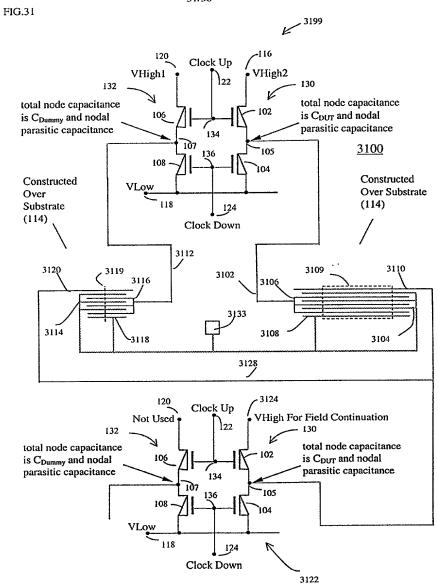
Patent Application for. CLUCKED BASED METHOD AND DEVICES FOR MEA: ...ING VOLTAGE-VARIABLE CAPACITANCES AND OTHER ON-CHIP PARAMETERS Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70



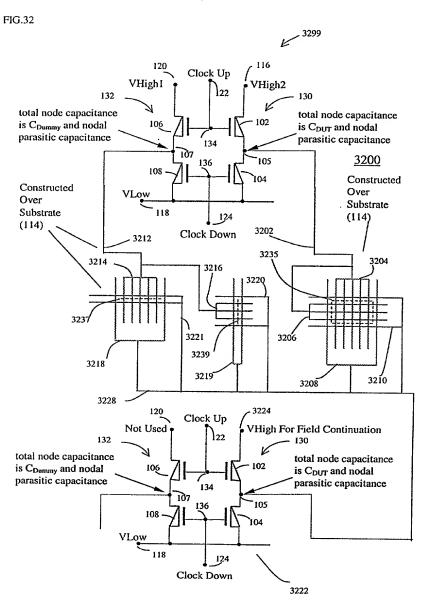


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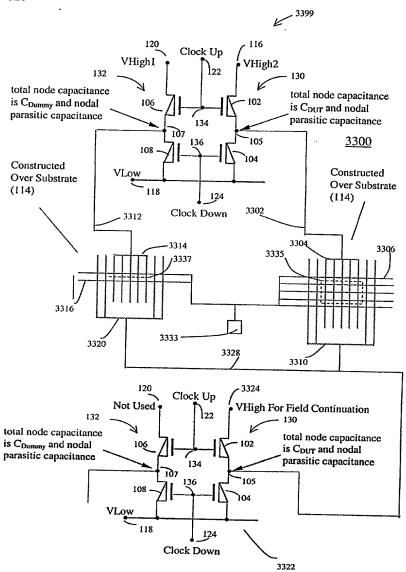


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Attorney Docket No.: 9076/70

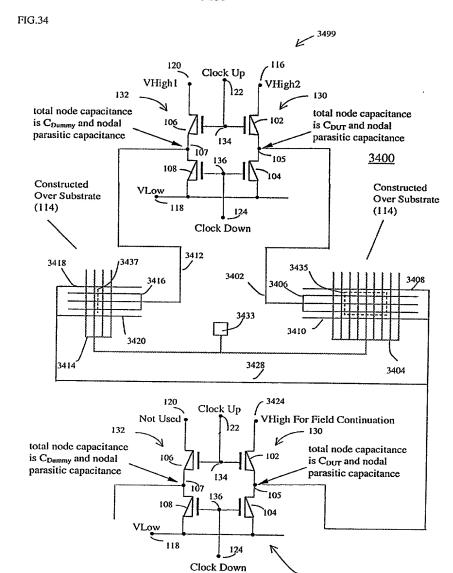




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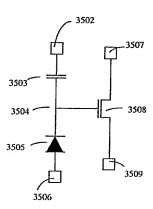
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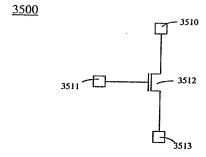
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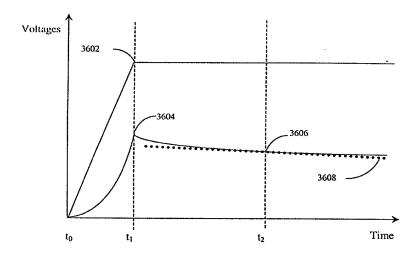
FIG. 35





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FIG. 36

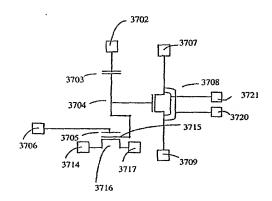


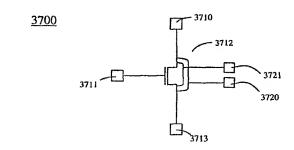
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FIG. 37





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FIG. 38

